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**POU920000198US1**

Amendment to the Specification:

Amend paragraph number [0002] of the published application as follows:

“HIGH-PERFORMANCE MEMORY QUEUE”, Chen et al., (IBM Docket No. POU920000185US1), Ser. No. 09/790,853, filed February 22, 2001, and published on August 22, 2002, as Pub. No. US 2002/0116538 A1.